Attorney Docket No.: 042390.P7462D Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Parat, Krishna et al.

Serial No.: Not yet assigned

Filed: Herewith

For: A NOVEL FLASH INTEGRATED CIRCUIT AND ITS METHOD OF FABRICATION

Examiner: Not yet assigned

Art Unit: Not yet assigned

Assistant Commissioner for Patents
and Trademarks
Washington, D.C. 20231

FORMAL DRAWINGS SUBMISSION

Enclosed herewith for submission in the United States Patent and Trademark Office are twelve (12) sheets of formal drawings for the patent application referenced above.

If there are any further charges please charge them to Deposit Account No. 02-2666. A duplicate of this sheet is enclosed for that purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 5/23, 2001

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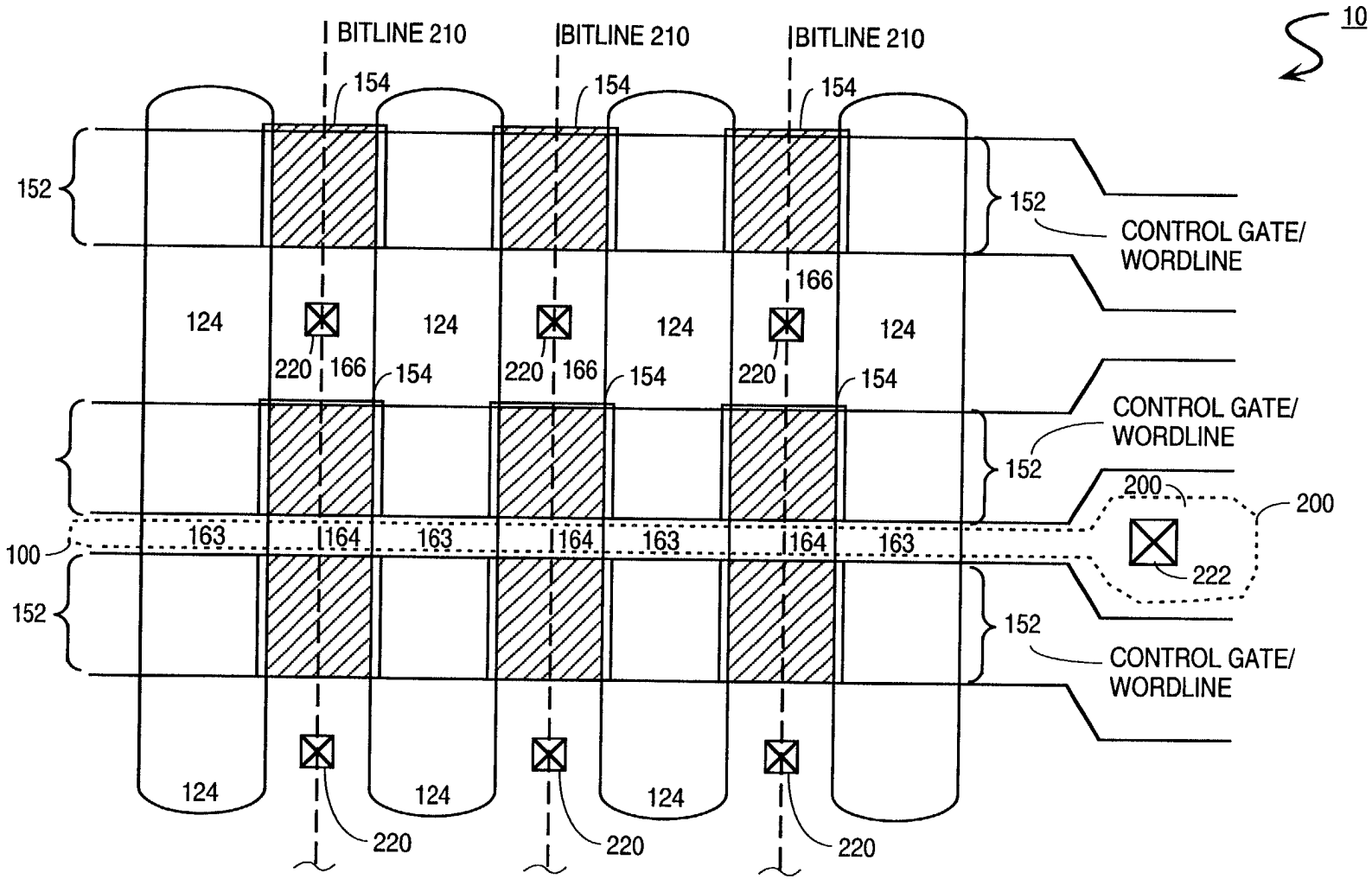

FIG. 1A

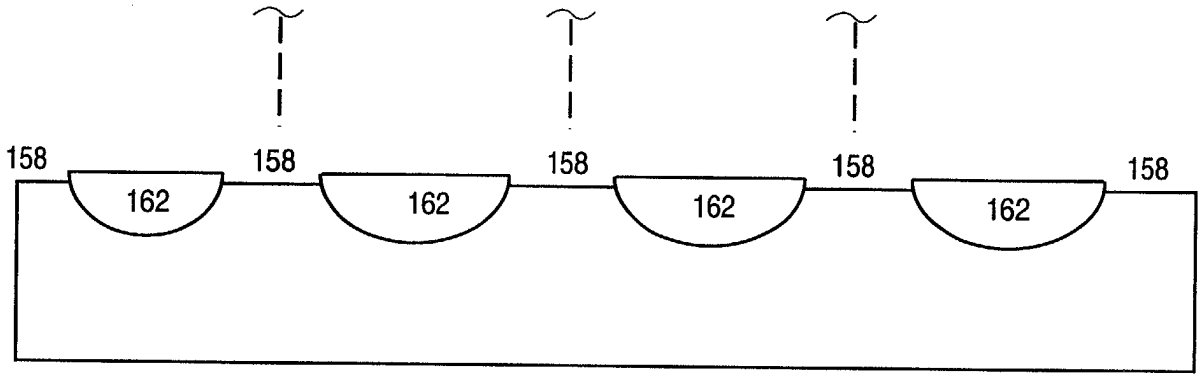

FIG. 1B

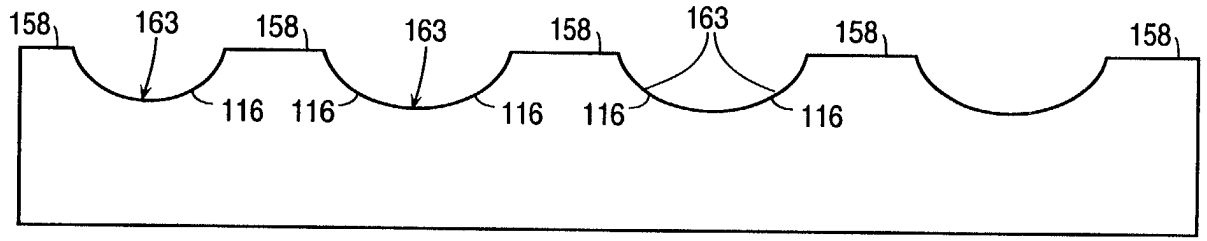

FIG. 1C

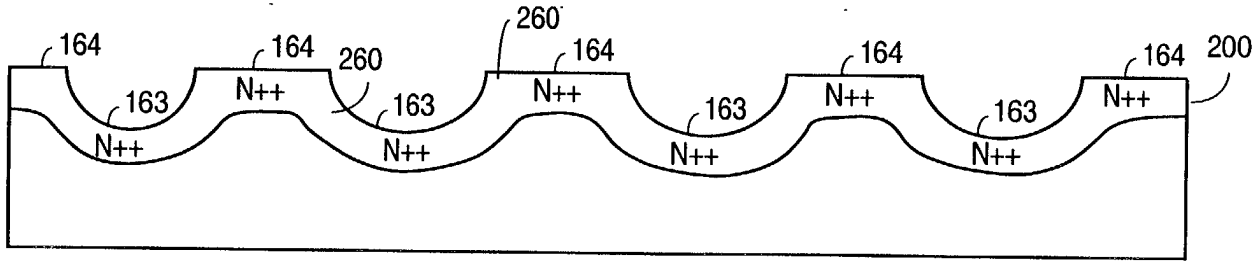

FIG. 1D

Patent Application Publication

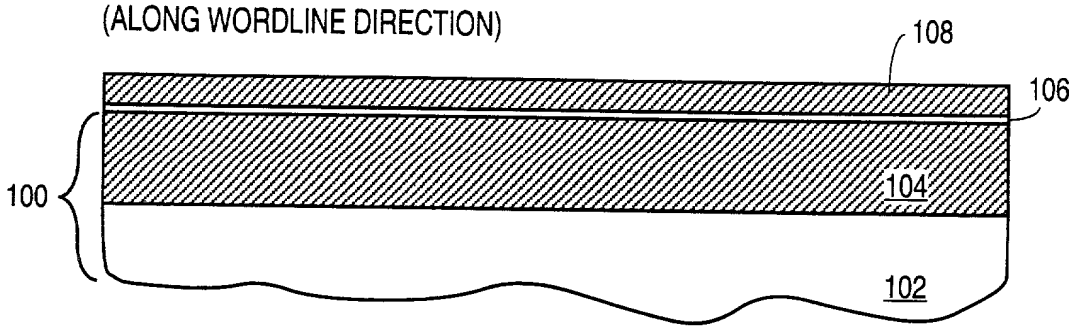

FIG. 2

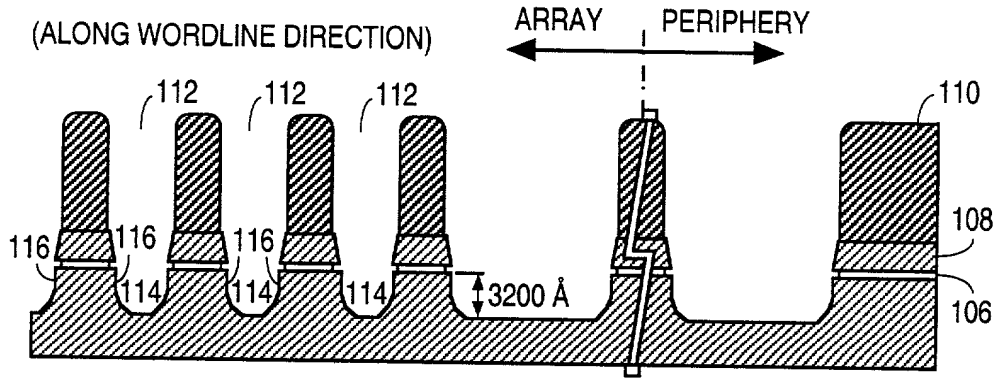

FIG. 3

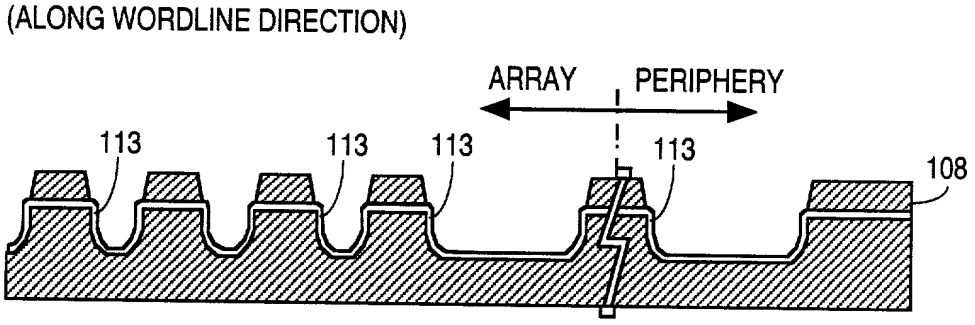

FIG. 4

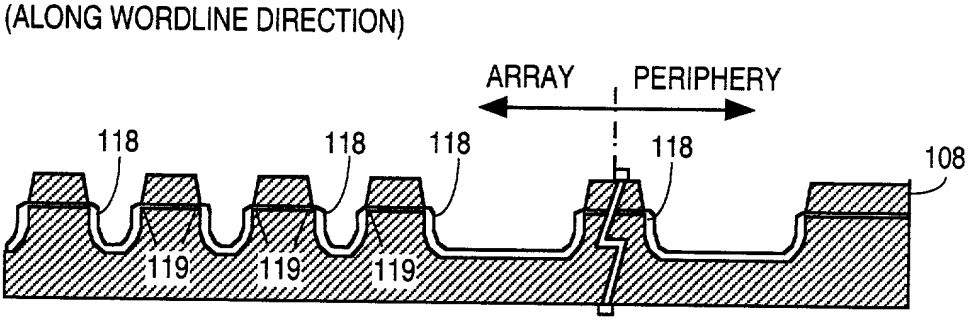

FIG. 5

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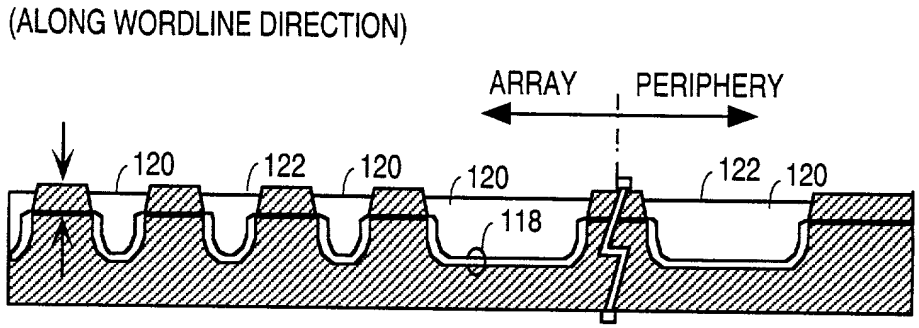

FIG. 6

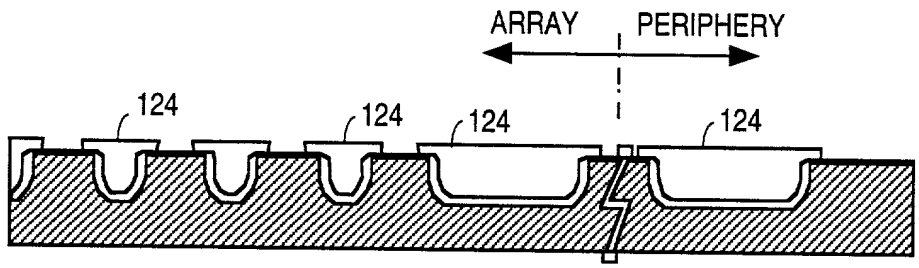

FIG. 7

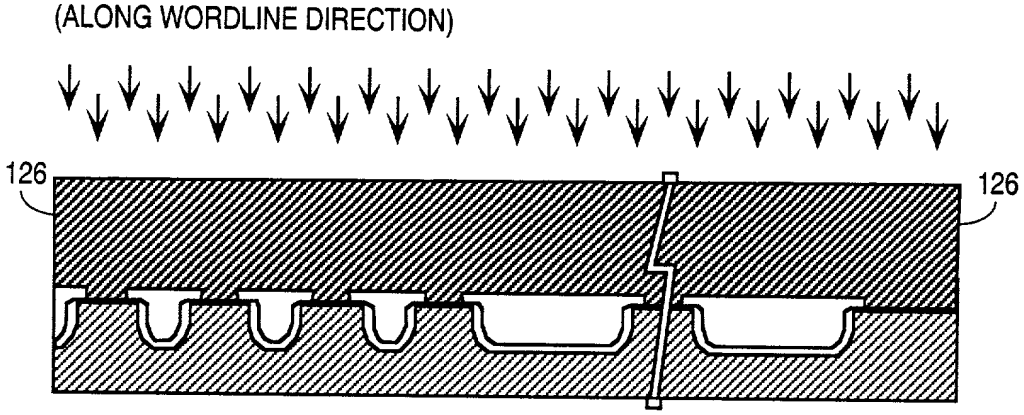

FIG. 8

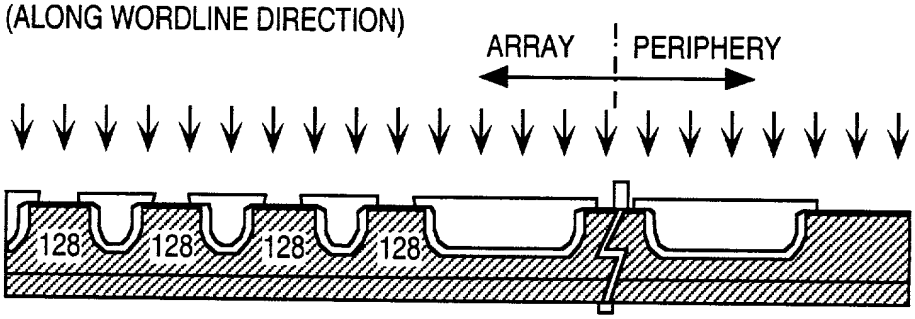

FIG. 9

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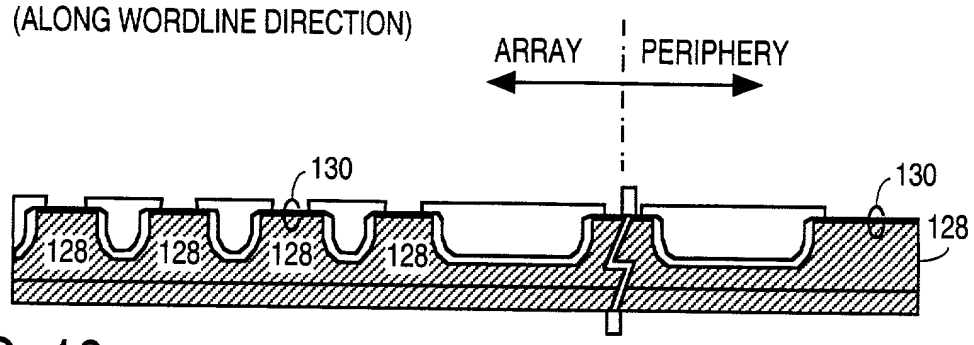

FIG. 10

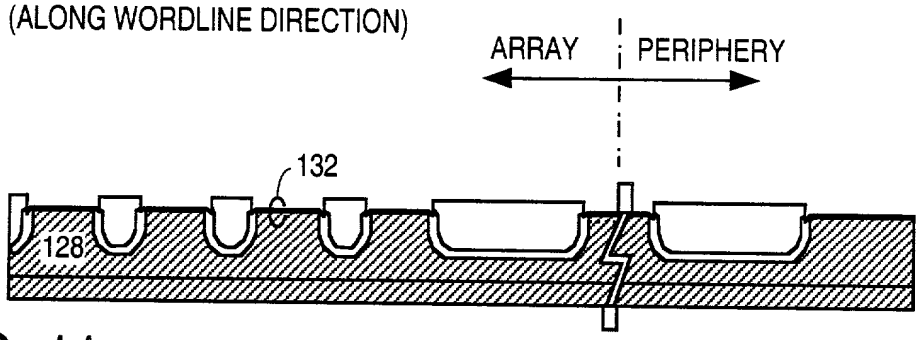

FIG. 11

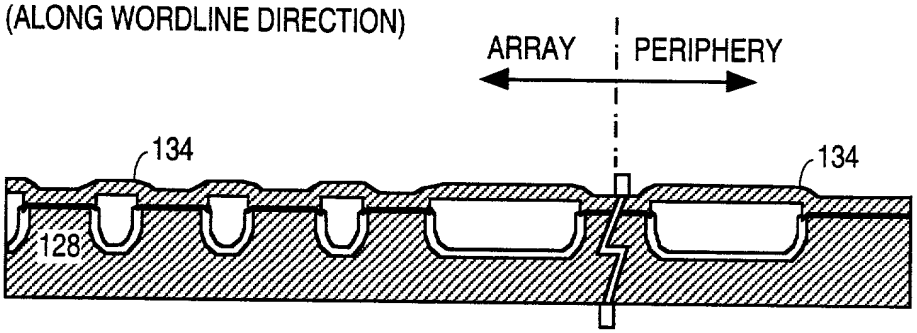

FIG. 12

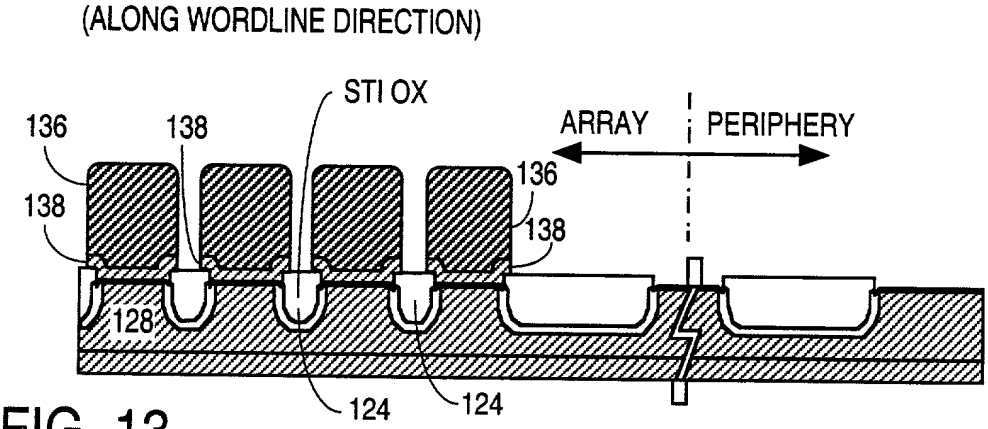

FIG. 13

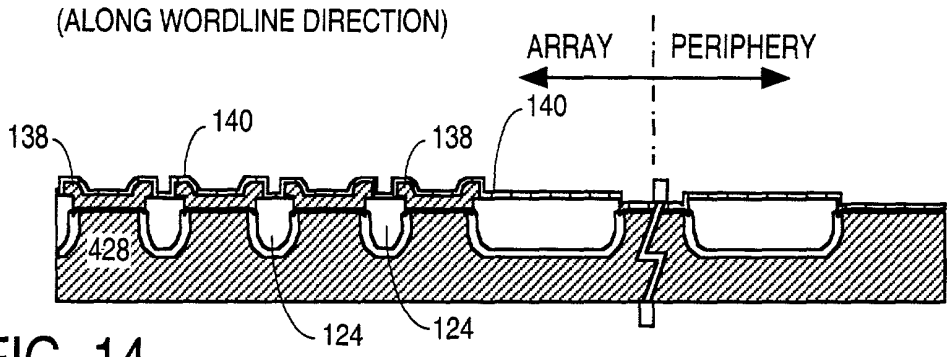

FIG. 14

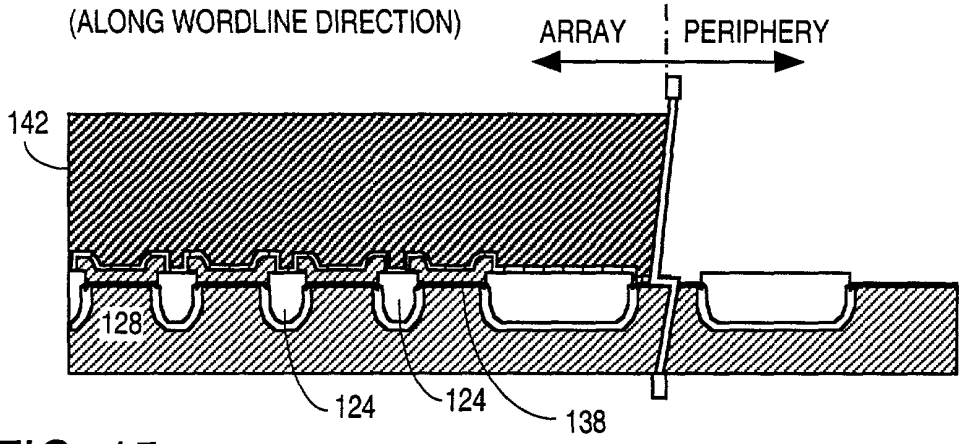

FIG. 15

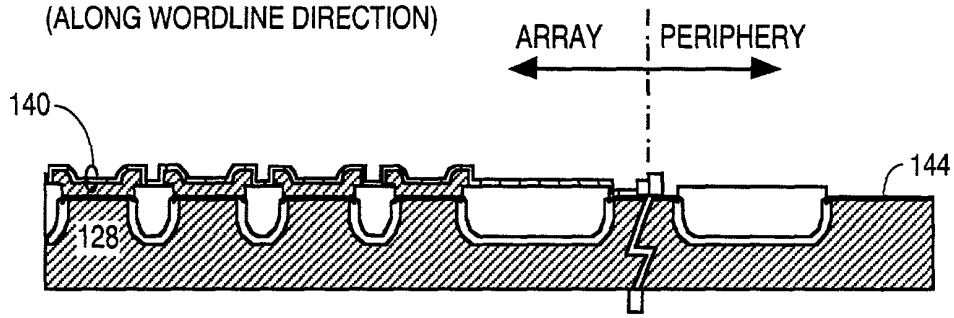

FIG. 16

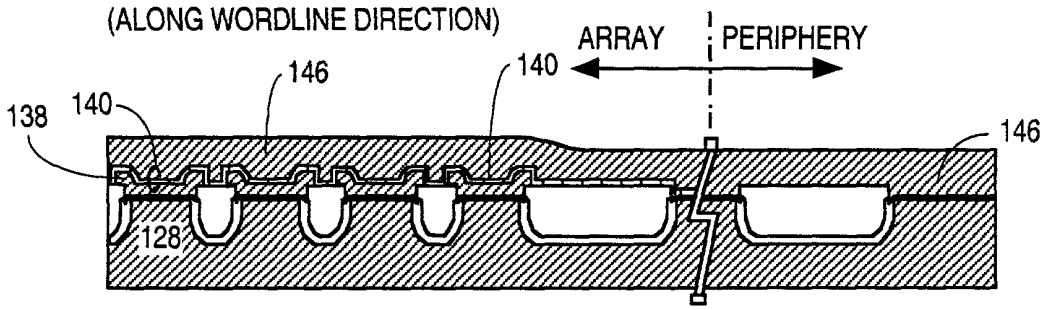

FIG. 17

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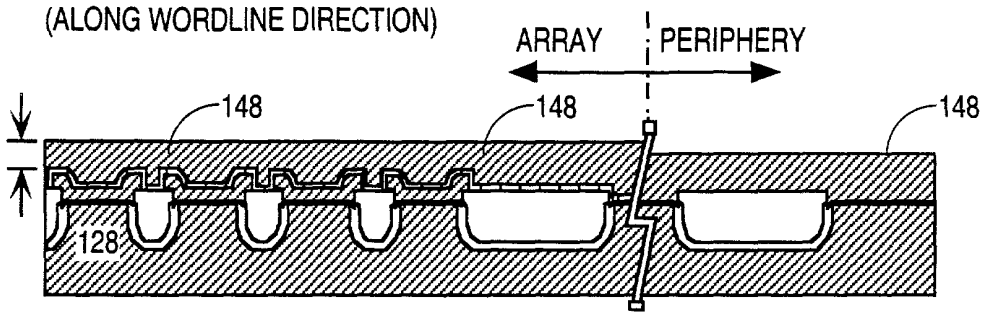

FIG. 18

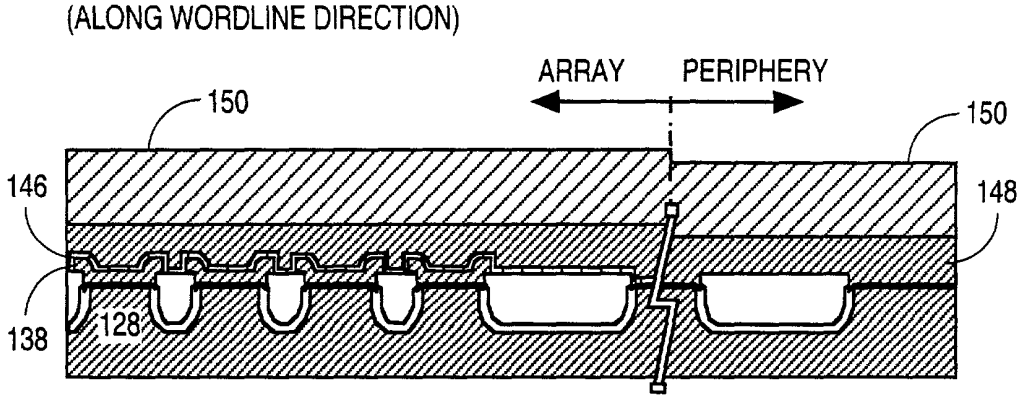

FIG. 19A

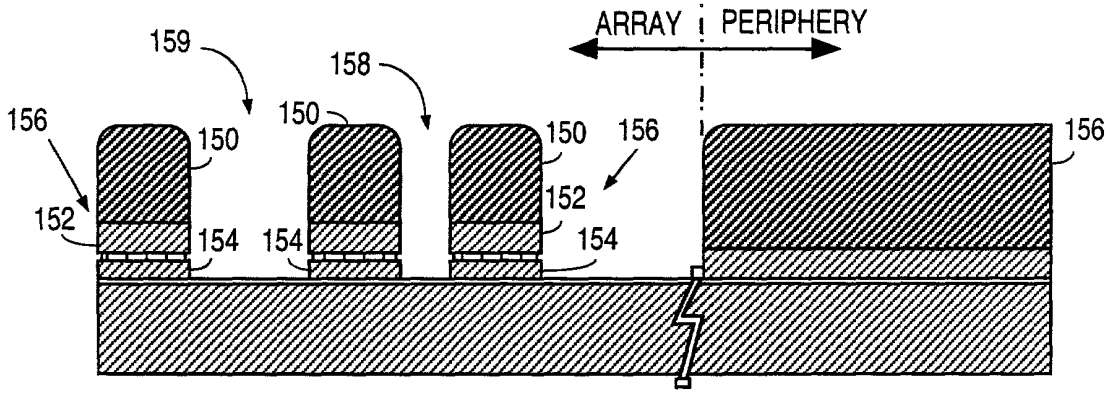

FIG. 19B

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THROUGH S/D IN BITLINE DIRECTION

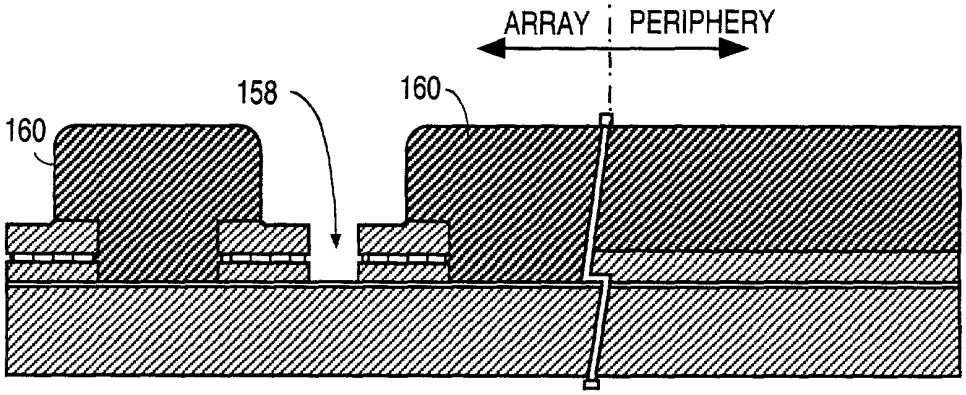

FIG. 20A

THROUGH STI IN BITLINE DIRECTION

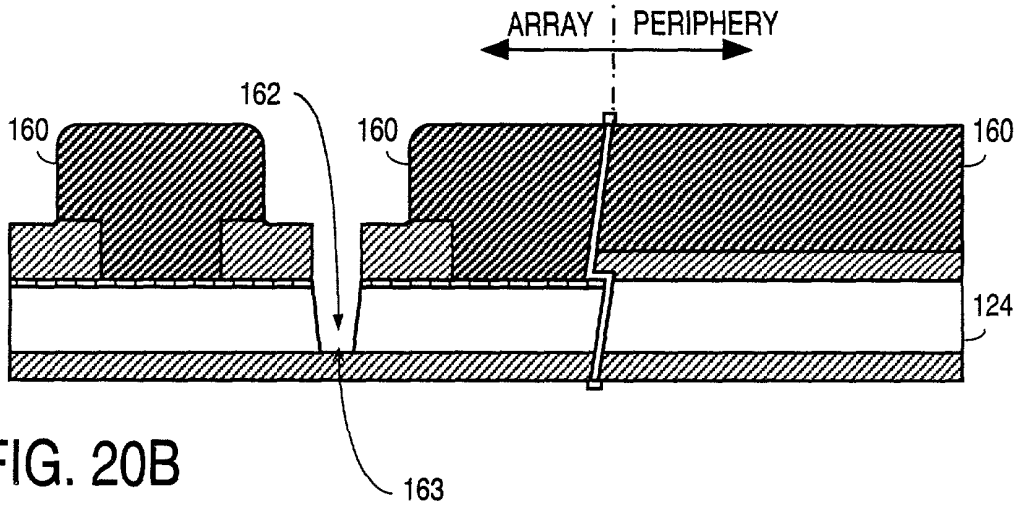

FIG. 20B

ALONG BITLINE DIRECTION

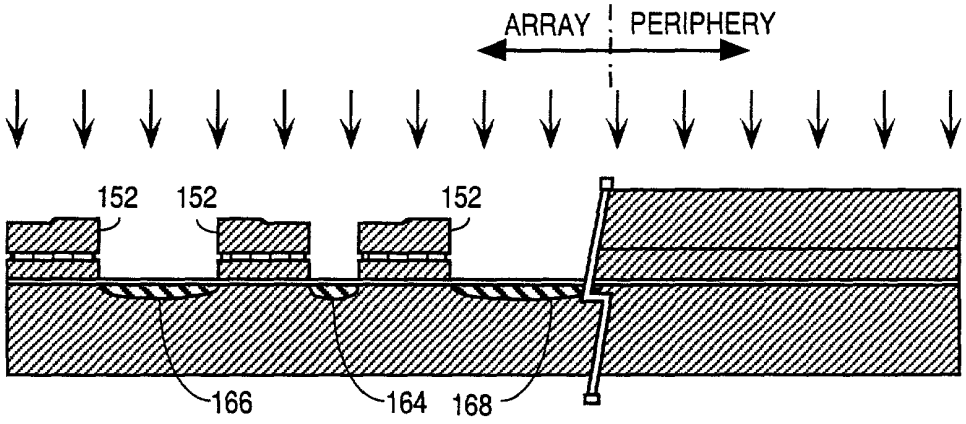

FIG. 21

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ALONG BITLINE DIRECTION

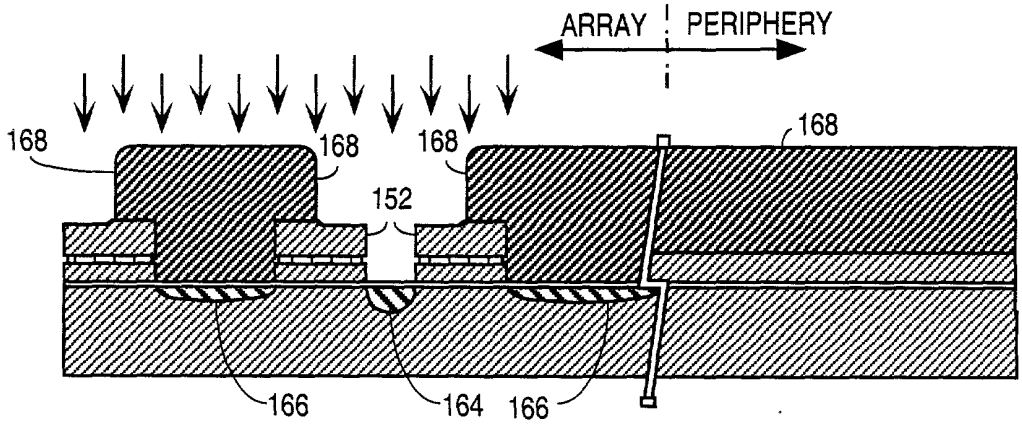

FIG. 22

ALONG BITLINE DIRECTION

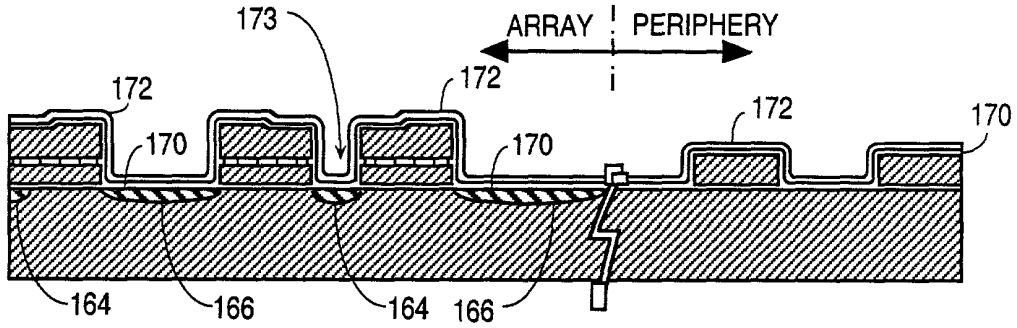

FIG. 23

ALONG BITLINE DIRECTION

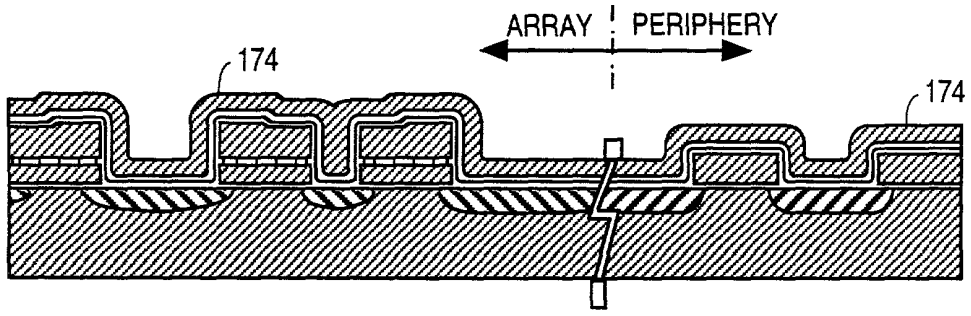

FIG. 24

Docket No. 1303.1001

ALONG BITLINE DIRECTION

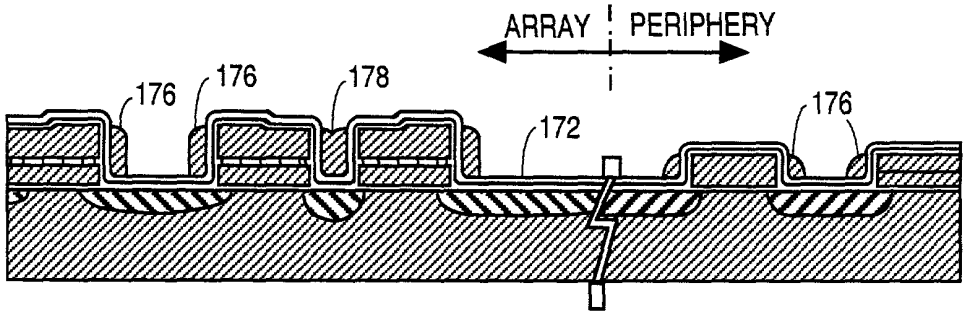

FIG. 25

ALONG BITLINE DIRECTION

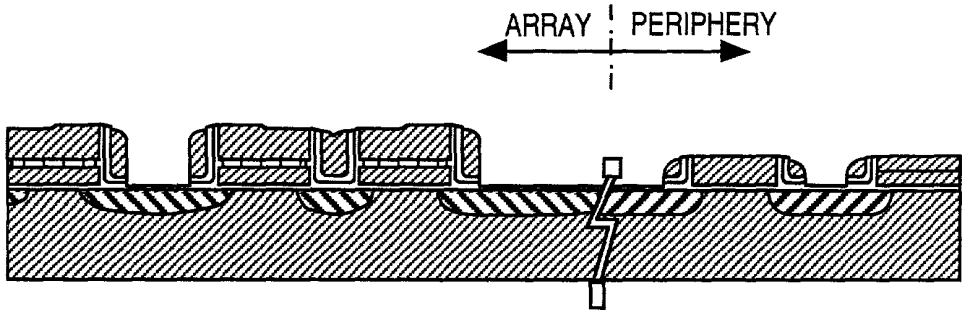

FIG. 26

ALONG BITLINE DIRECTION

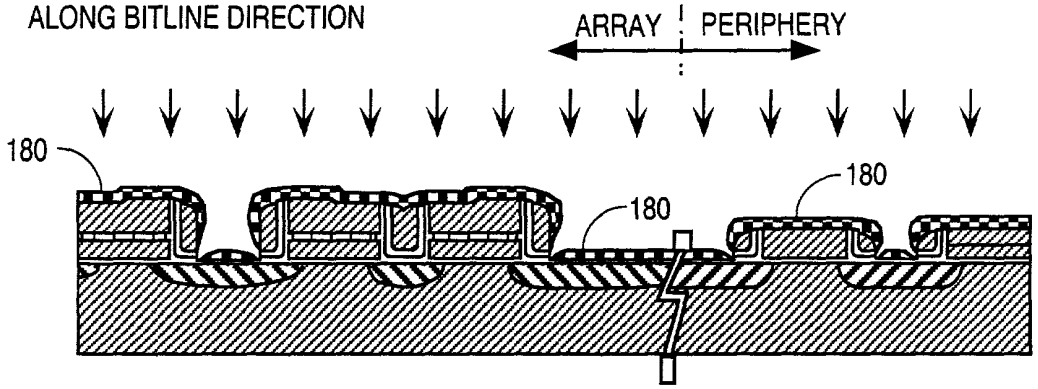

FIG. 27

Docket No. 5050-0003

Patent Application Publication

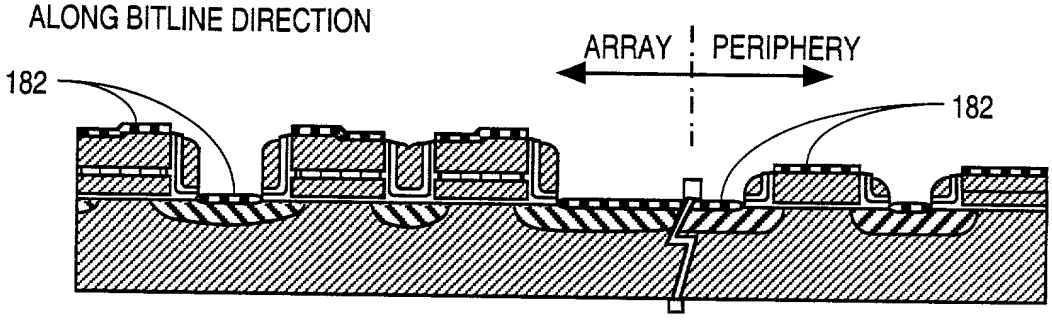

FIG. 28

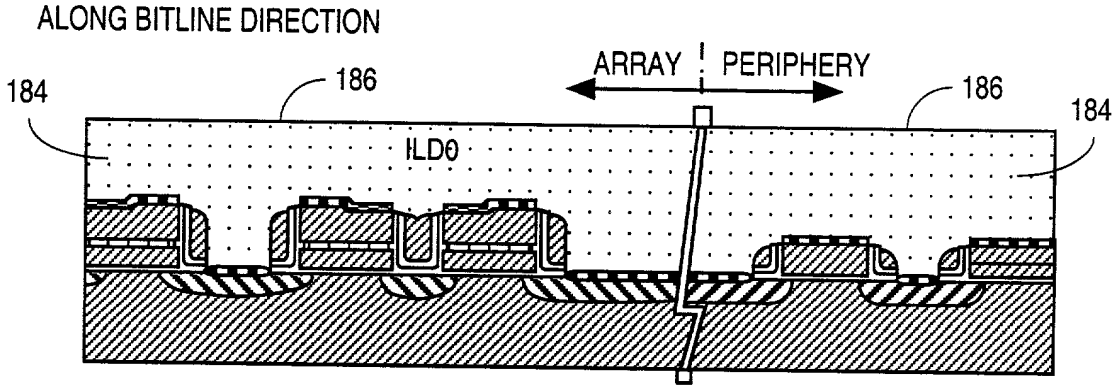

FIG. 29

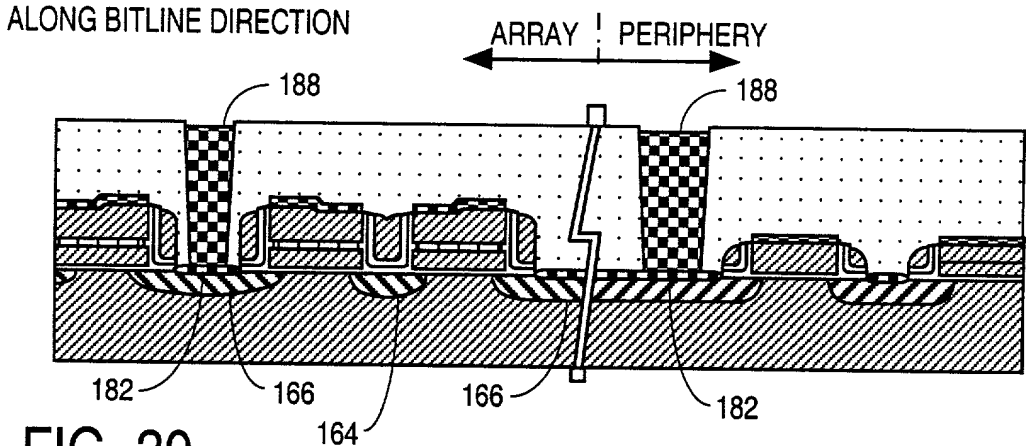

FIG. 30

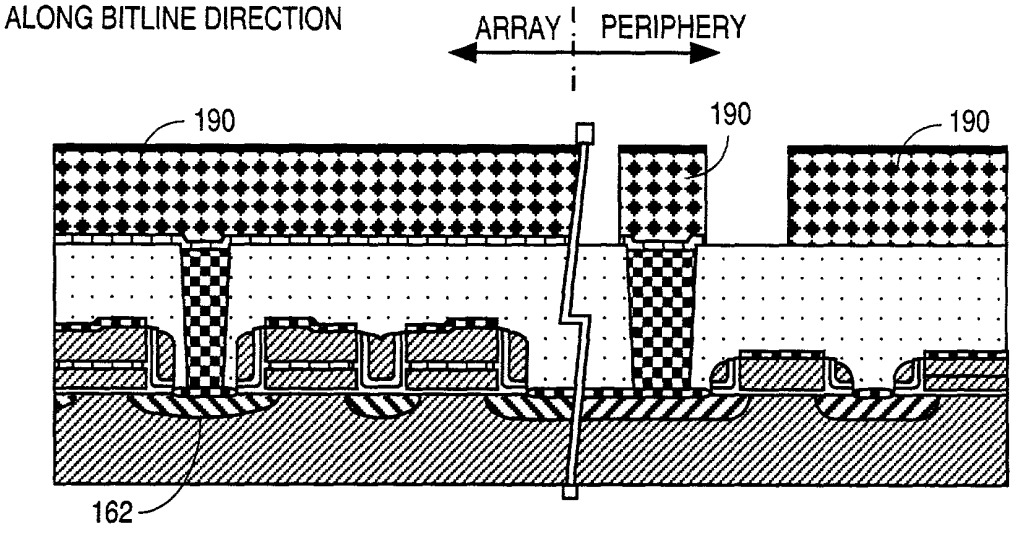

FIG. 31

US 6,555,858 B1